

**AMENDMENTS TO THE CLAIMS**

Please amend the claims as follows.

1. (Currently Amended) An integrated circuit, comprising:
  - a signal driver that generates a signal on a signal path;
  - a first wire disposed adjacent to the signal path; and
  - shield control circuitry that, after a transition on the signal path, ~~causes~~generates a  
~~value on~~ the first wire to transition to a value that causes a charge up of capacitance between the signal path and the first wire, wherein a subsequent transition on the signal path causes a discharge of capacitance between the signal path and the first wire.
2. (Canceled)
3. (Previously Presented) The integrated circuit of claim 1, further comprising a capacitor having one terminal operatively connected to the signal path and another terminal operatively connected to the first wire.
4. (Original) The integrated circuit of claim 1, wherein the signal driver is a transistor.
5. (Original) The integrated circuit of claim 1, wherein the signal driver is a gate.
6. (Original) The integrated circuit of claim 1, wherein the shield control circuitry is dependent on the signal driver.

7. (Previously Presented) The integrated circuit of claim 1, further comprising a second wire disposed adjacent to the signal path, wherein the first and second wires are used to shield the signal path.
8. (Original) The integrated circuit of claim 7, wherein the shield control circuitry comprises inverting circuitry that outputs onto the first and second wires dependent on a synchronous signal input to the shield control circuitry.
9. (Original) The integrated circuit of claim 7, wherein the shield control circuitry comprises inverting circuitry that outputs onto the first and second wires dependent on an asynchronous signal input to the shield control circuitry.
10. (Original) The integrated circuit of claim 7, wherein the shield control circuitry comprises:
  - a delay element; and
  - inverting circuitry that outputs onto the first and second wires.
11. (Previously Presented) The integrated circuit of claim 10, wherein the delay element generates a delay greater than a signal propagation delay of the signal.
12. (Previously Presented) An integrated circuit, comprising:
  - driving means for generating a signal on a signal path; and
  - shielding control means for actively controlling a value on wires shielding that signal path such that the driving means only participates in discharge events.

13. (Currently Amended) A method for non-interactively driving a signal on a signal path, comprising:
- after the signal on the signal path has transitioned to a first voltage potential,
- charging a capacitor by ~~causing~~transitioning a wire to transition to a second voltage potential, wherein the wire shields the signal path; and
- discharging the capacitor when the signal path transitions to the second voltage potential.
14. (Previously Presented) The method of claim 13, wherein the capacitor has one terminal operatively connected to the wire and another terminal operatively connected to the signal path.
15. (Original) The method of claim 13, further comprising selectively delaying the driving of the wire to the second potential.